

### **REMARKS**

Claims 18-28 are presently pending. Claims 18-27 were rejected. Claim 28 and 29 are added. Reconsideration and continued examination are respectfully requested.

Claim 18 were rejected for double patenting under 35 U.S.C. 101 and under 35 U.S.C. 102 as being anticipated by Lee.

Assignee respectfully traverses the rejection under 35 U.S.C. 101. It is noted that claim 18 differs from claim 9 of U.S. Patent 7,284,072 in a variety of ways. For example, claim 18 recites, "fetching ... a second portion of the range after fetching the first portion, wherein the second portion of the range has a lower address than the first portion" that is not recited in claim 9 of U.S. Patent 7,284,072.

Claim 18 recites, among other limitations "a single command to provide a specified range of a plurality of sequential data words". Examiner has indicated that Lee teaches "a single command (fig. 5, control command 501; and fig. 6, command 601) to provide a range of sequential data words (col. 4, lines 24-29, access memory blocks)".

Assignee respectfully traverses the rejection. Figure 5 and 6 are merely "a view illustrating the structure of a coprocessor operation control command that controls a CODEC block in DSCU described in Fig. 4. Fig. 6 is a view illustrating the structure of a coprocessor command used for external memory access of CODEC blocks in DSCU". Col. 4, Lines 24-29.

It is noted that 501 and 601 do not include the capability to specify a range of data words. See Col. 10, Line 59 - Col. 11, Line 36. Accordingly, Lee does not teach

"a single command to provide a ***specified range of a plurality of sequential data words***".

Additionally, claim 18 also recites, among other limitations, "fetching a first portion of the range and a second portion of the range after fetching the first portion, wherein the second portion of the range has a lower address than the first portion".

Examiner has indicated that Lee teaches "a first portion (fig. 11, portion of VLD input buffer 1112) of the range and a second portion (fig. 11, portion of VLD input buffer 1112) of the range after fetching the first portion". Assignee respectfully traverses because the mere fact that Lee discloses VLD input buffer 1112 is not a teaching of "fetching a first portion ... and a second portion".

Accordingly, for at least the foregoing reasons, Assignee respectfully requests that Examiner withdraw the rejection to claims 18 and 24, and dependent claims 19-23 and 25-28.

**CONCLUSION**

For at least the foregoing reasons, Assignee respectfully submits that each of the pending claims are allowable and Examiner is respectfully requested to pass this case to issuance. The Commissioner is hereby authorized to charge additional fees or credit overpayments to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

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Respectfully submitted,



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